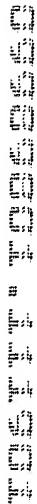


**Figure 1**

A block diagram illustrating a system architecture. At the top is a rectangular block labeled **Processor**, with the reference numeral **200** pointing to its top-right corner. Below the Processor is another rectangular block labeled **Memory**, with the reference numeral **205** pointing to its top-left corner. A vertical double-headed arrow connects the bottom center of the Processor block to the top center of the Memory block, indicating bidirectional communication between them.

## Figure 2



### Figure 3